

Modi Institute of Technology, Kota
Ist Midterm
III Year VI Sem
Branch: Computer Science & Engineering
Sub: Computer Network

Time: 1 Hr

MM: [10]

Attempt any two questions. Each question carries five Marks.

- Q1. Define TCP/IP model in detail?
- Q2. Discuss the network layer design issue?
- Q3. Write short note on (any two)?
- (a) Distance vector routing algorithm.
 - (b) Link state routing algorithm.
 - (c) Shortest path algorithm.

Best of Luck

Modi Institute of Technology, Kota
Ist Midterm
III Year VI Sem
Branch: Computer Science & Engineering
Sub: Design & Analysis of Algorithms

Time: 1 Hr

MM: [10]

Attempt any two questions. Each question carries five Marks.

- Q1. Describe the various types of notations with example?
- Q2. Find optimal solution for given data by knapsack problem
Consider $n=5$, $(W_1, W_2, W_3, W_4, W_5) = (5, 4, 6, 2, 1)$ $(P_1, P_2, P_3, P_4, P_5) = (5, 2, 2, 4, 5)$ and $M=12$.
- Q3. Solve the TSP (Traveling Salesman Problem) for the following cost matrix?

	x	y	z	w
x	A	10	15	20
y	5	A	9	10
z	6	13	A	12
w	8	8	9	A

Best of Luck

Modi Institute of Technology, Kota
B.Tech. VI Sem III Year I Mid Term Exam Subject: Computer Graphics&MT

Attempt any two.[2x5=10]

- Q1. Define Computer graphics and any 4 related terms. Explain applications of CGMT.
- Q2. Explain CRT with help of neat diagram along with its architecture
- Q3. Explain with example DDA or Bresenham's Line Drawing Algorithm.

Modi Institute of Technology, Kota
Ist Midterm
III Year VI Sem
Branch: Computer Science & Engineering
Sub: Human Computer Interface

Time: 1 Hr

MM: [10]

Attempt any two questions. Each question carries five Marks.

- Q.1 Compare various text entry devices?
- Q.2 How do ergonomics affects the interaction between man and machine? Explain.
- Q.3 Describe the Design process in detail.

Best of Luck

Modi Institute of Technology, Kota
Ist Midterm
III Year VI Sem
Branch: Computer Science Engineering
Sub: Embedded System Design

Time:1 Hr

MM:[10]

Attempt any two questions. Each question carries five marks.

- Q1. Describe Embedded System Design & its block diagram.
- Q2. Explain the design process of embedded system.
- Q3. Write short notes on (Any Two)
- A). Timing Diagram
 - B). Direct Memory Access
 - C). UARTs and PLDs

Best of Luck

Modi Institute of Technology, Kota
Ist Midterm
III Year VI Sem
Branch: Computer Science & Engg.
Sub: Theory of Computation

Time: 1 Hr

MM: [10]

Attempt any two questions. Each question carries five marks.

Q1. Define the Finite Automata, Explain difference between NFA & DFA with example.

Q2. Differentiate Mealy and Moore Machine with example.

Q3. Convert the following Moore Machine into its equivalent Mealy Machine.

Present State	Next State		Output
	a	b	
Q0	Q1	Q2	1
Q1	Q3	Q4	1
Q2	Q4	Q0	0
Q3	Q1	Q2	0
Q4	Q3	Q0	1

Best of Luck